fairlight **ENGINEERING CHANGE NOTE:** 𝔪𝔣𝔛𝟛

ECN #:   376

ASSEMBLY: BMW4011D   **DESCRIPTION:** MW4-PXY Dual CPU Card

New Assembly: **DATE:** 26/07/00

Current Assy Rev: J   **Current Schematic Rev:** 4.7

New Assy Rev: K   **New Schematic Rev:** 4.8

◉ **CMS BOMs Updated**   ◉ **Excel BOMs Updated**   ◉ **Subcontractor Records Updated**   **Compatibility Maintained:** Yes

IMPLEMENTATION INFORMATION:

○ **Safety Issue**   ○ **UL Compliance Issue**   ○ **New Feature**   ○ **Cost Reduction**
◉ **Bug Fix**   ○ **EMC Compliance Issue**   ○ **Quality Issue**
◉ **Reliability Reasons** ○ **Software Change**   ○ **Cosmetic Change**

REASON FOR CHANGE:

Fix arbitration which may cause system to hang.

DETAILS OF CHANGE:

1. Reprogram the CPLDs with the new file F05C19_4.CDF.

2.   Place revision resistors (RMD8573 - RES 10K 0.125W 1% SMD0603) as follows;
R127 - 10k
R128 - Omit
R133 - 10k
R134 - Omit
R135 - 10k
R136 - Omit
R137 - Omit
R138 - 10k

3. Update documentation and remark PCB with Rev K to reflect the change of assembly.